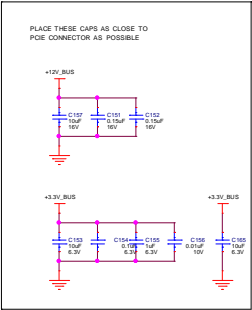


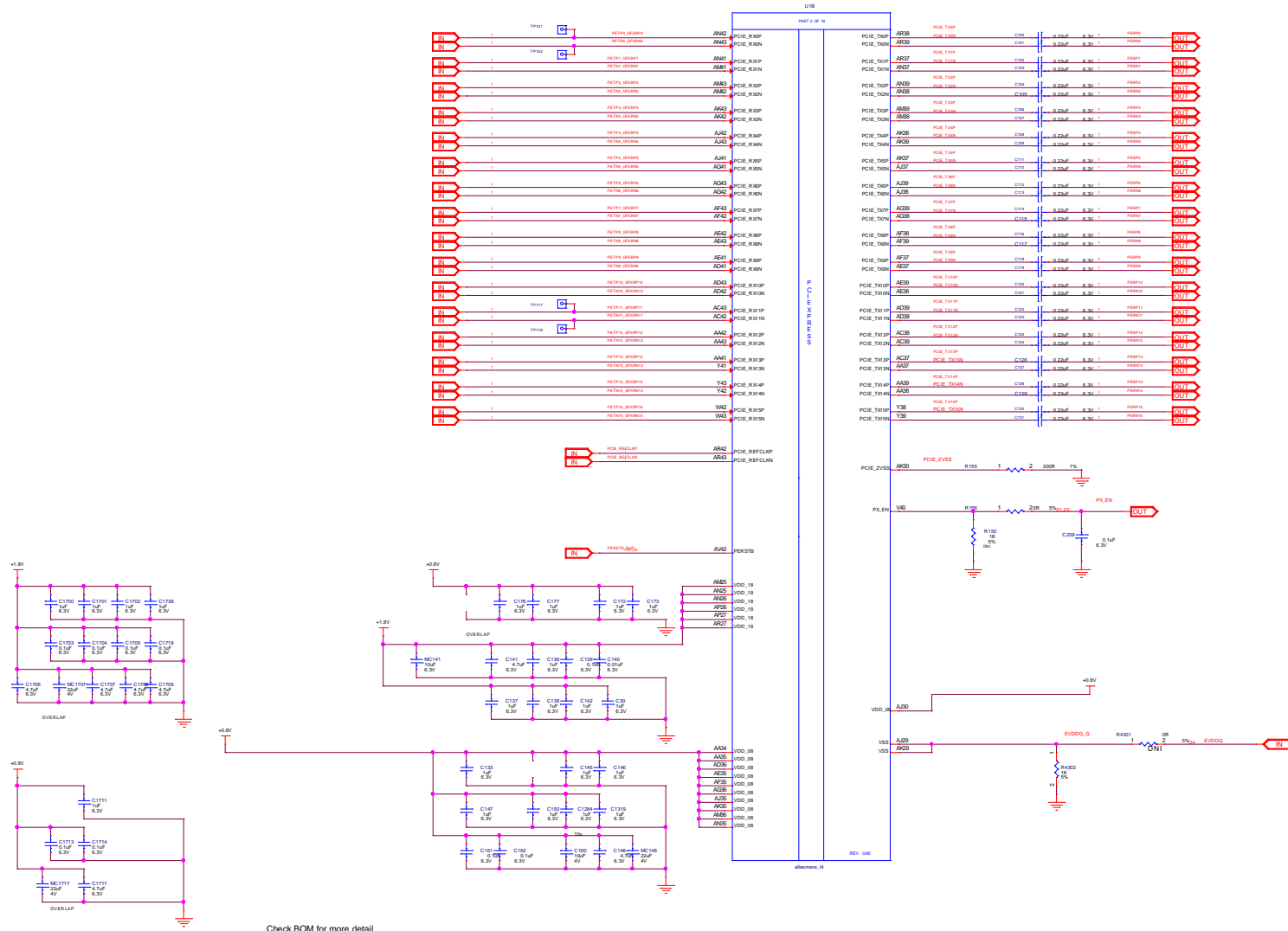


(1) PCI-EXPRESS EDGE CONNECTOR

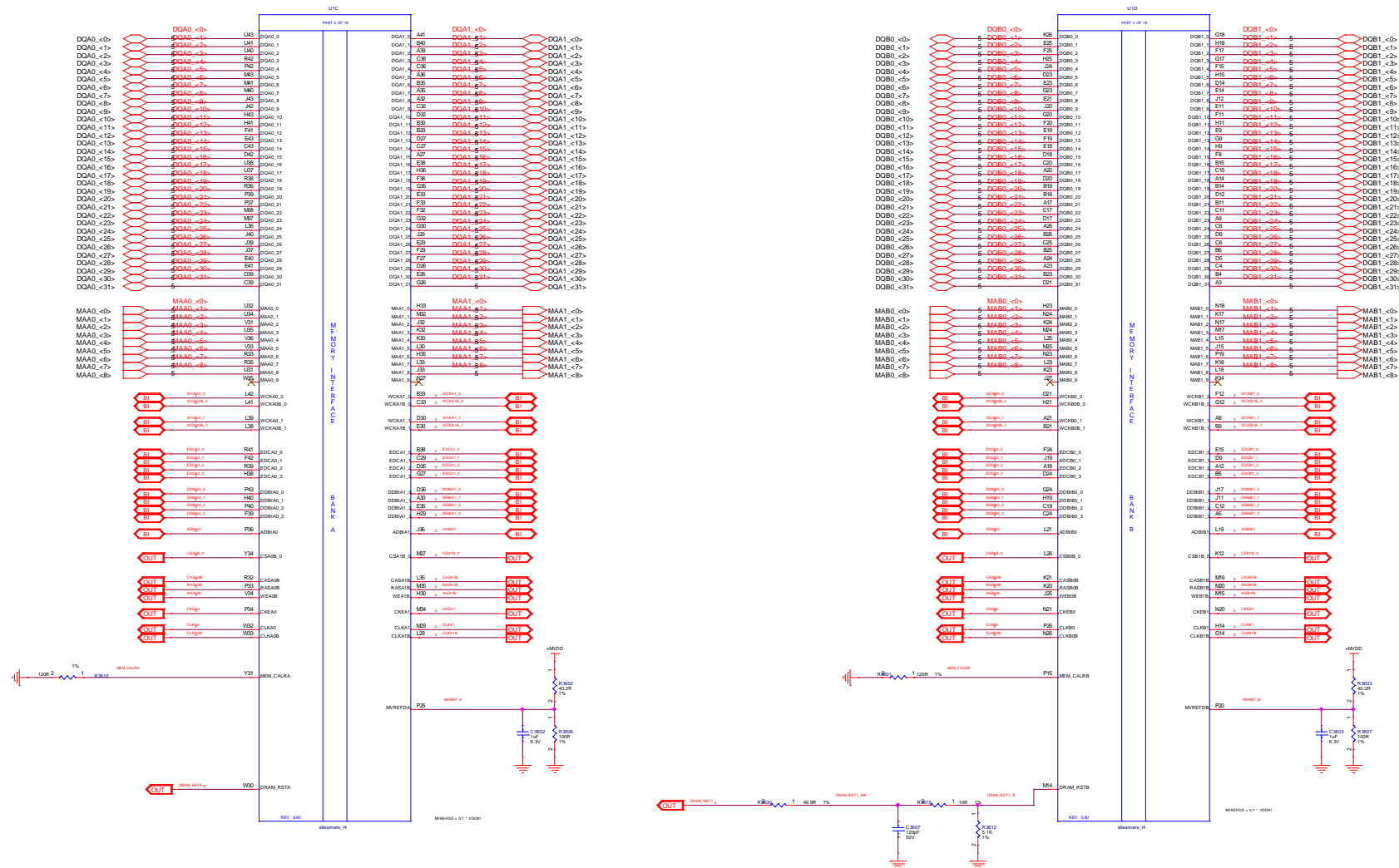


SYMBOL LEGEND	
IGN1	DOD NOT INSTALL
5-40 #	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

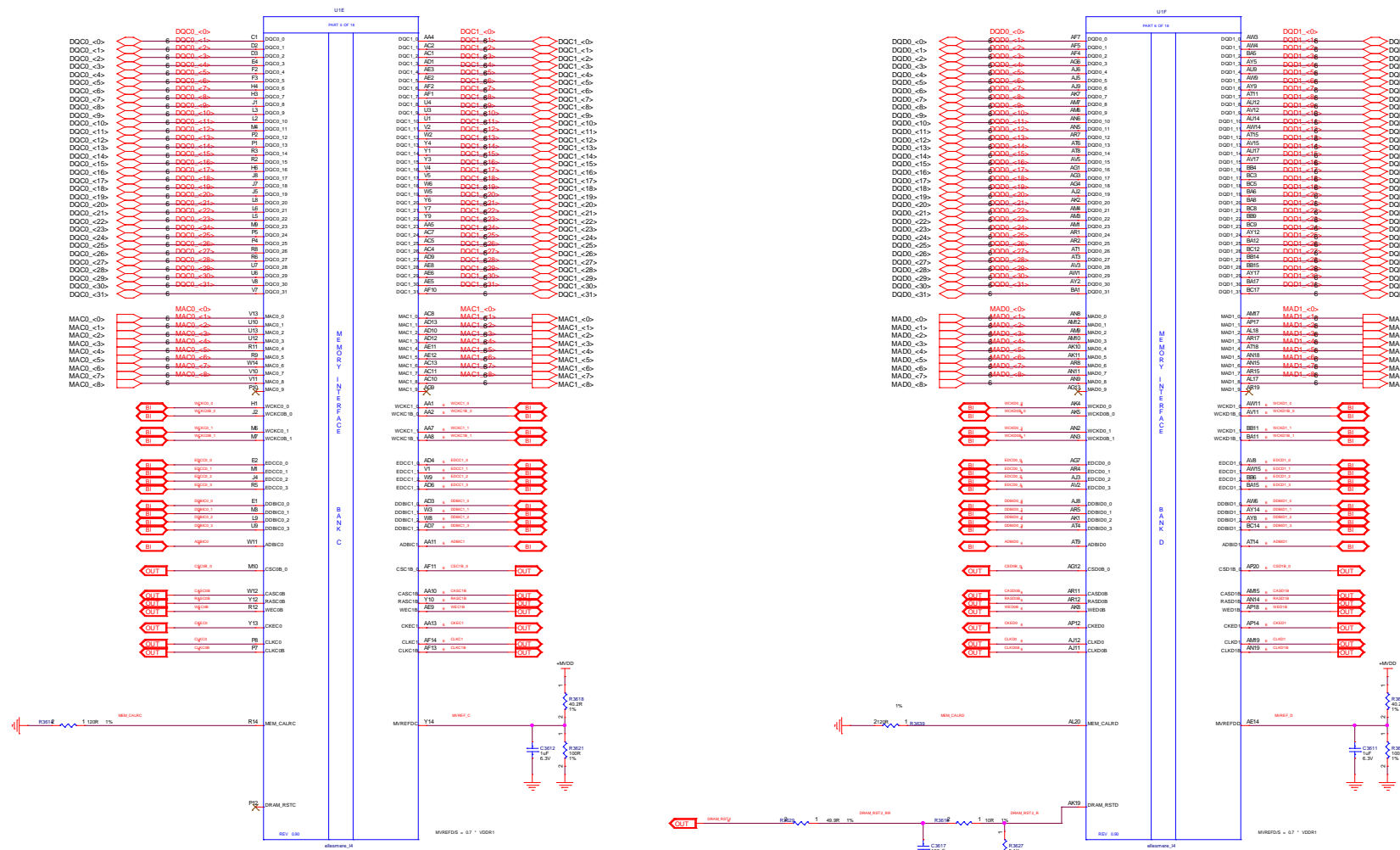
(2) ELLESMERE PCIE INTERFACE



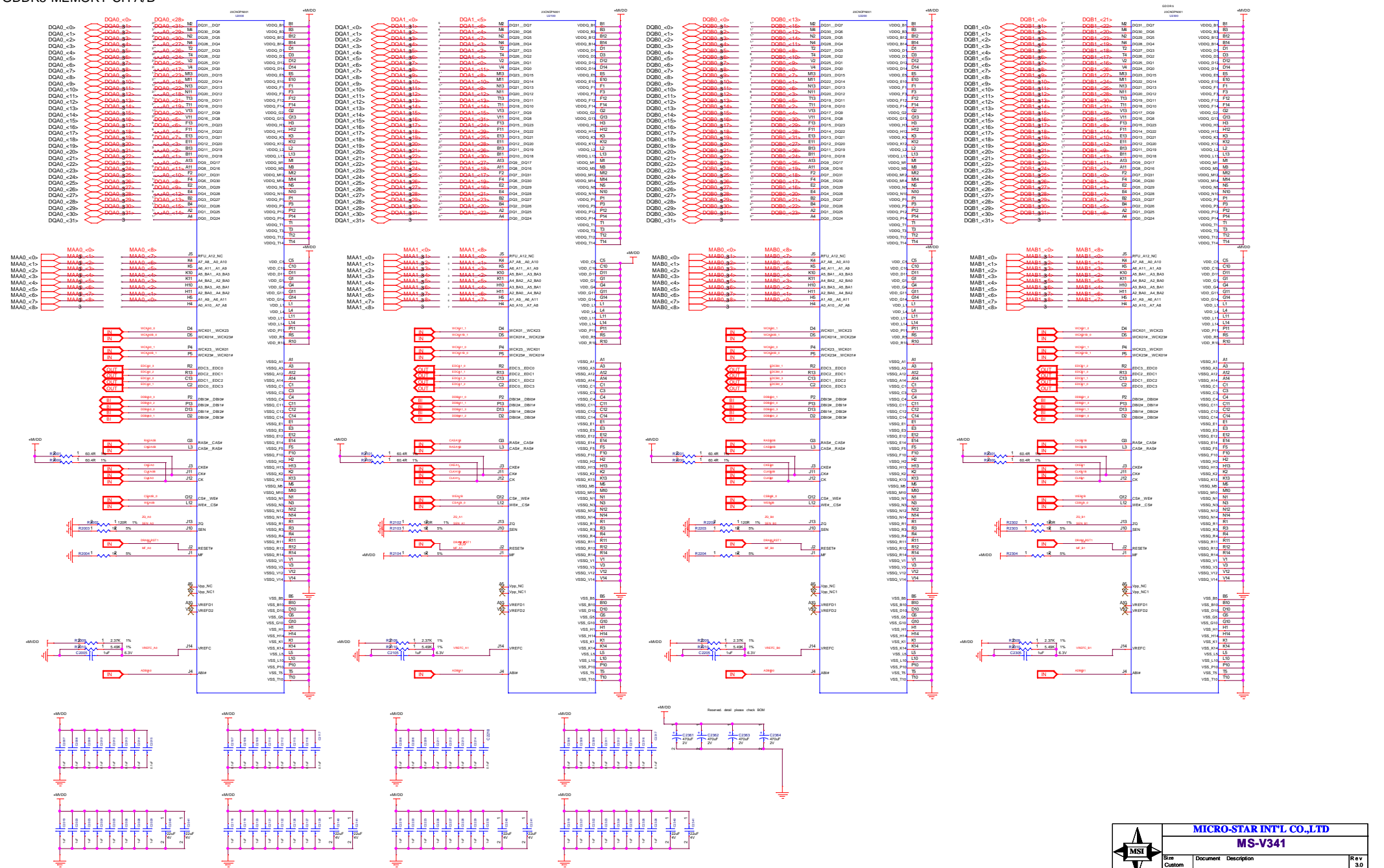
(3) ELLESMERE MEM INTERFACE CH A/B



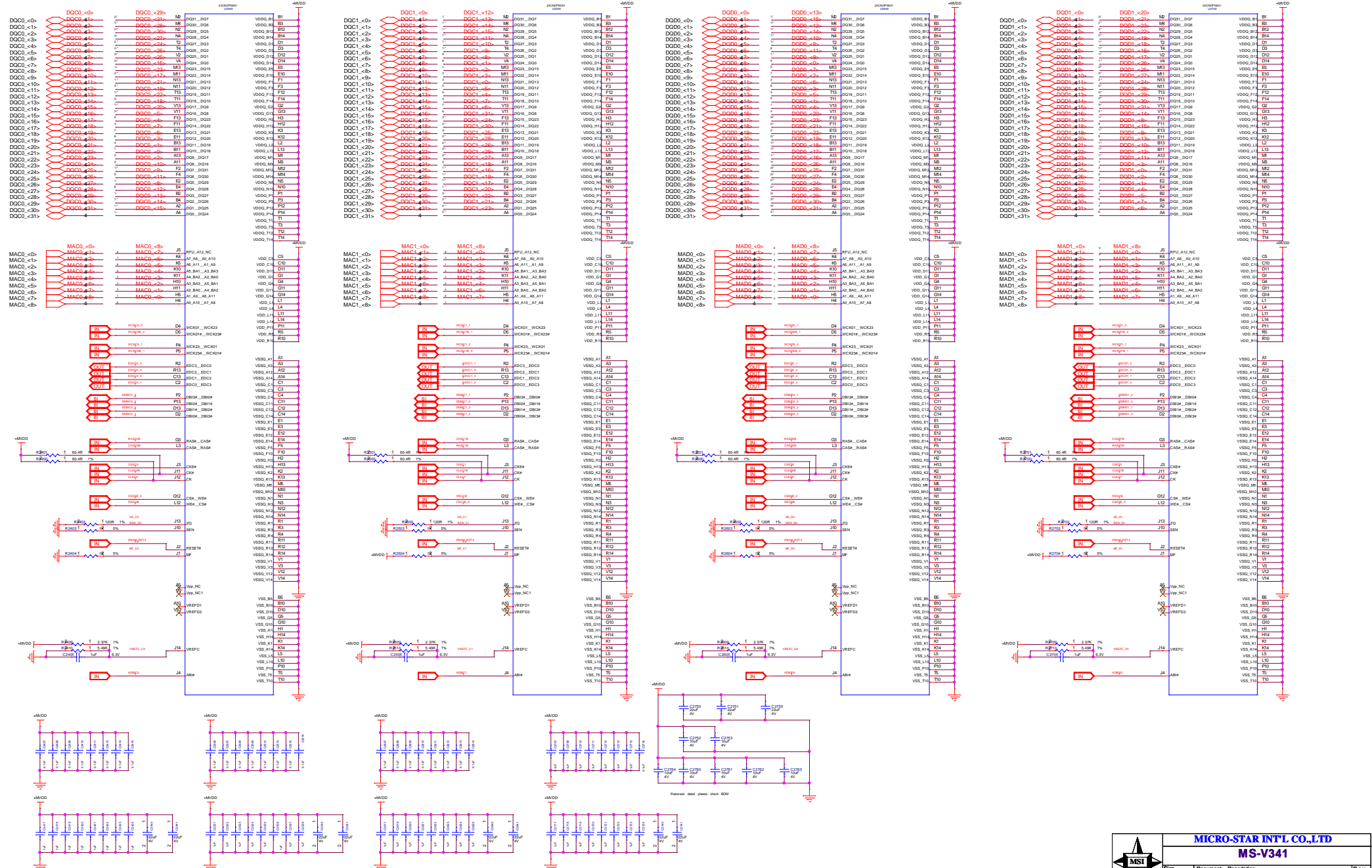
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B

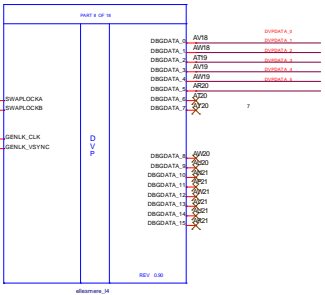
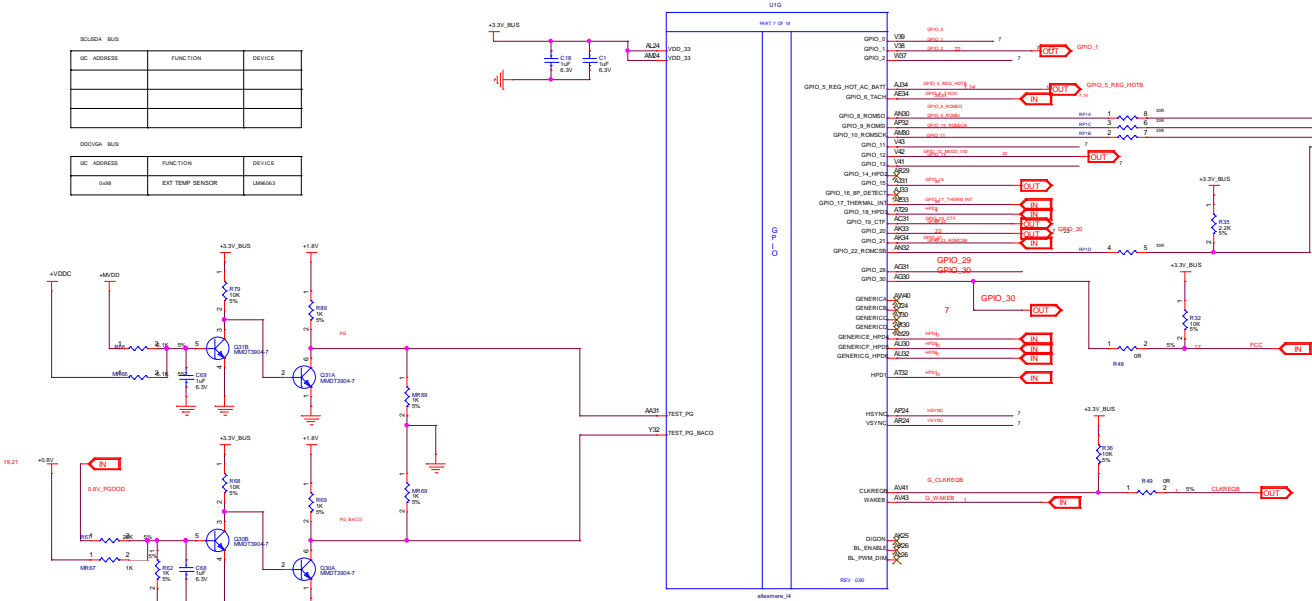


(6) GDDR5 MEMORY CH C/D

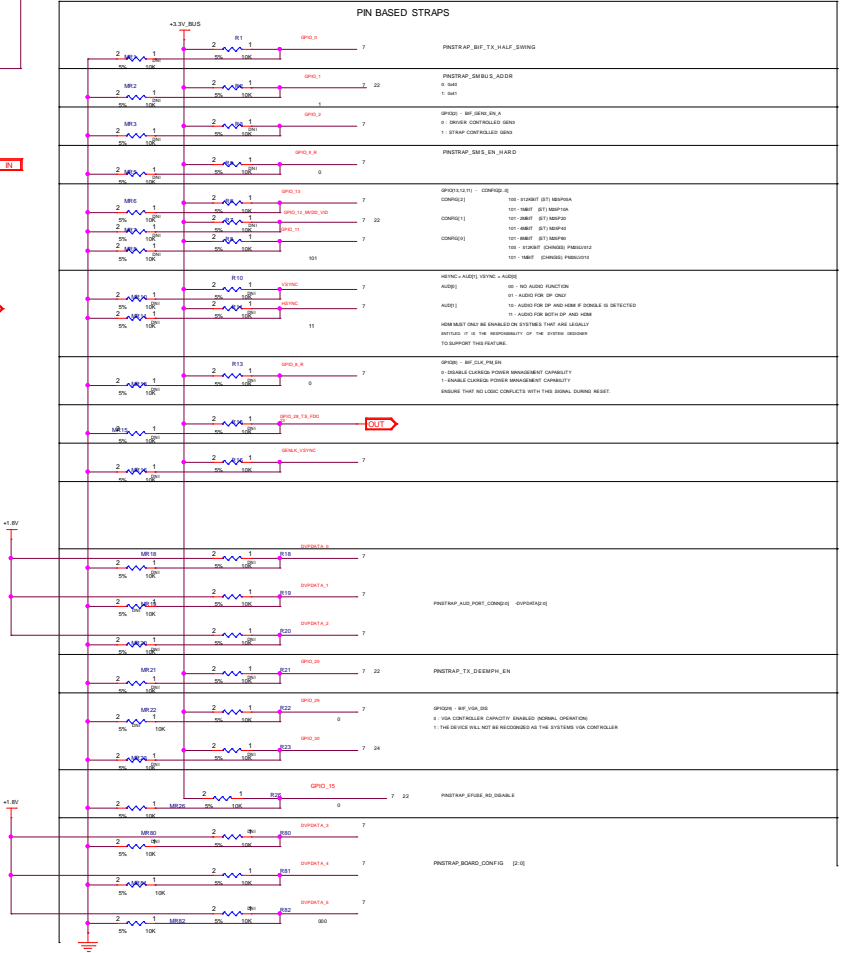
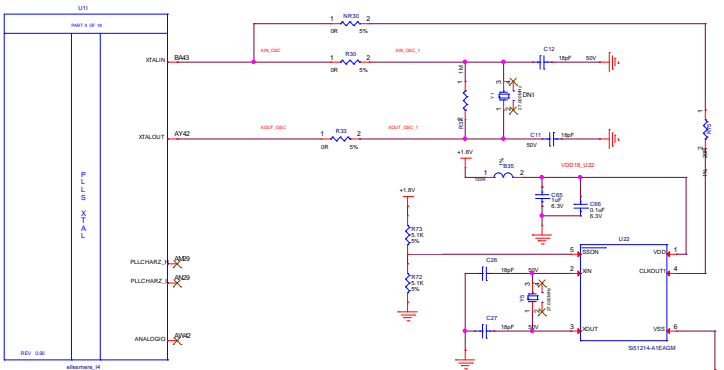


(7) ELLESMERE GPIO STRAP CF XTAL

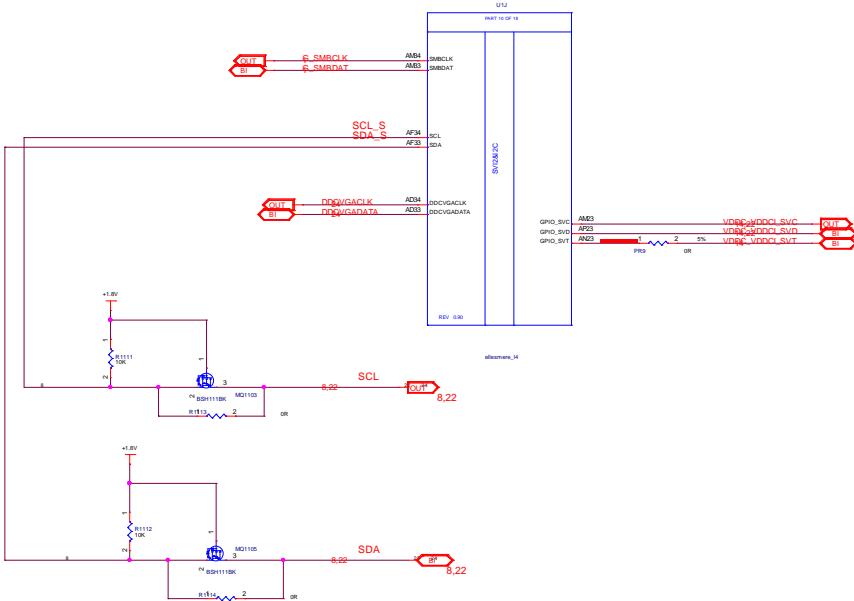
SCUDA BUS		
IC ADDRESS	FUNCTION	DEVICE
DOOVS4 BUS		
IC ADDRESS	FUNCTION	DEVICE
0400	EXT TEMP SENSOR	L980003



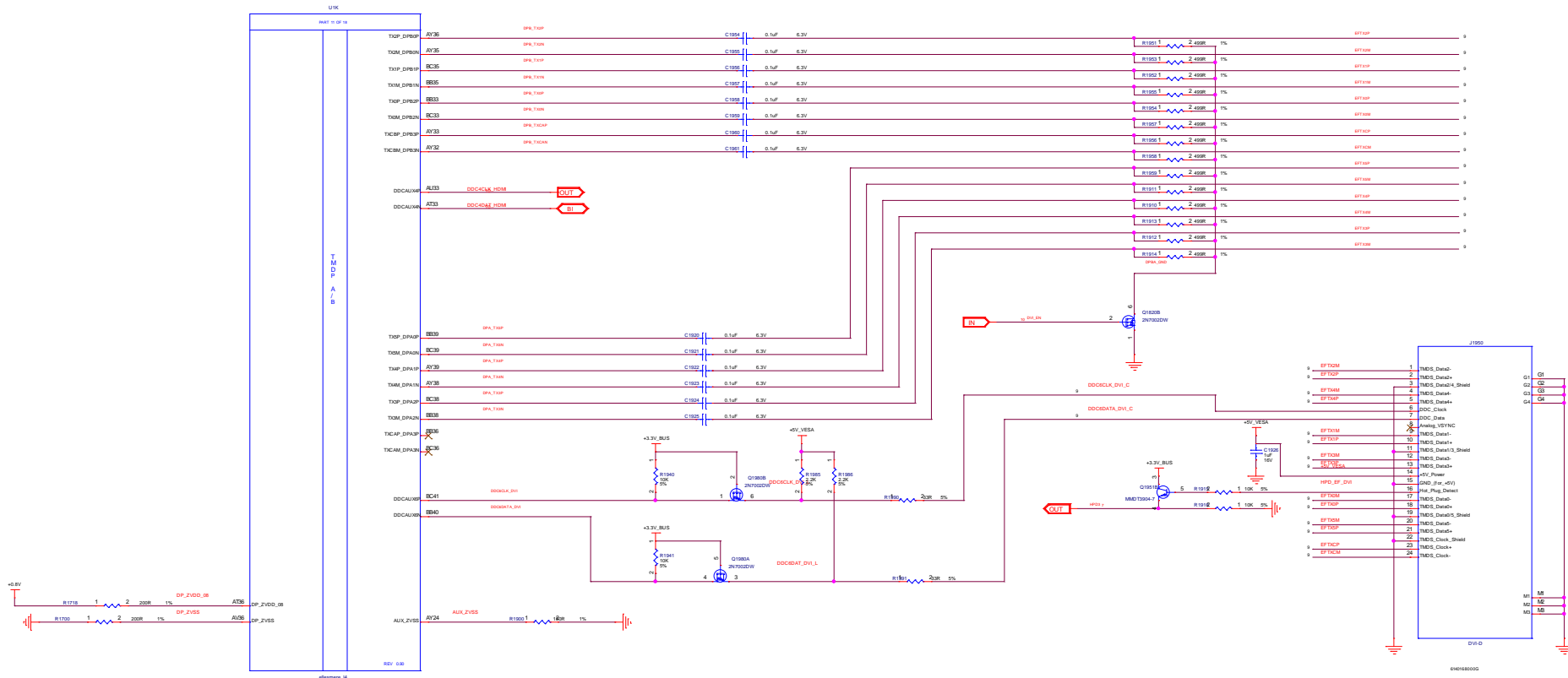
remove J2/J3



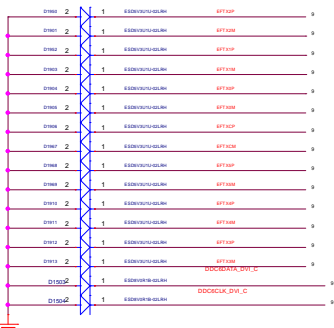
(8) ELLESMERE DAC1 LOCK

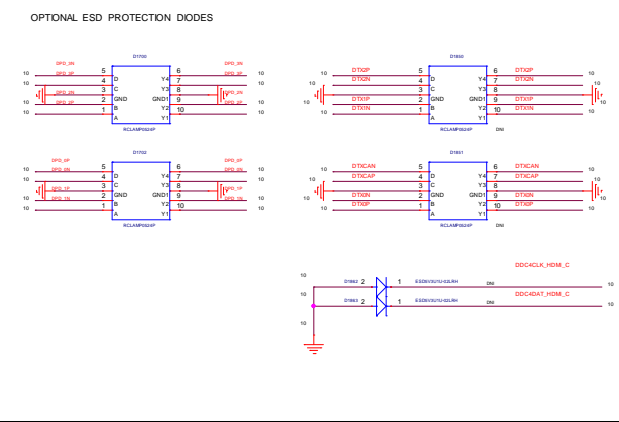
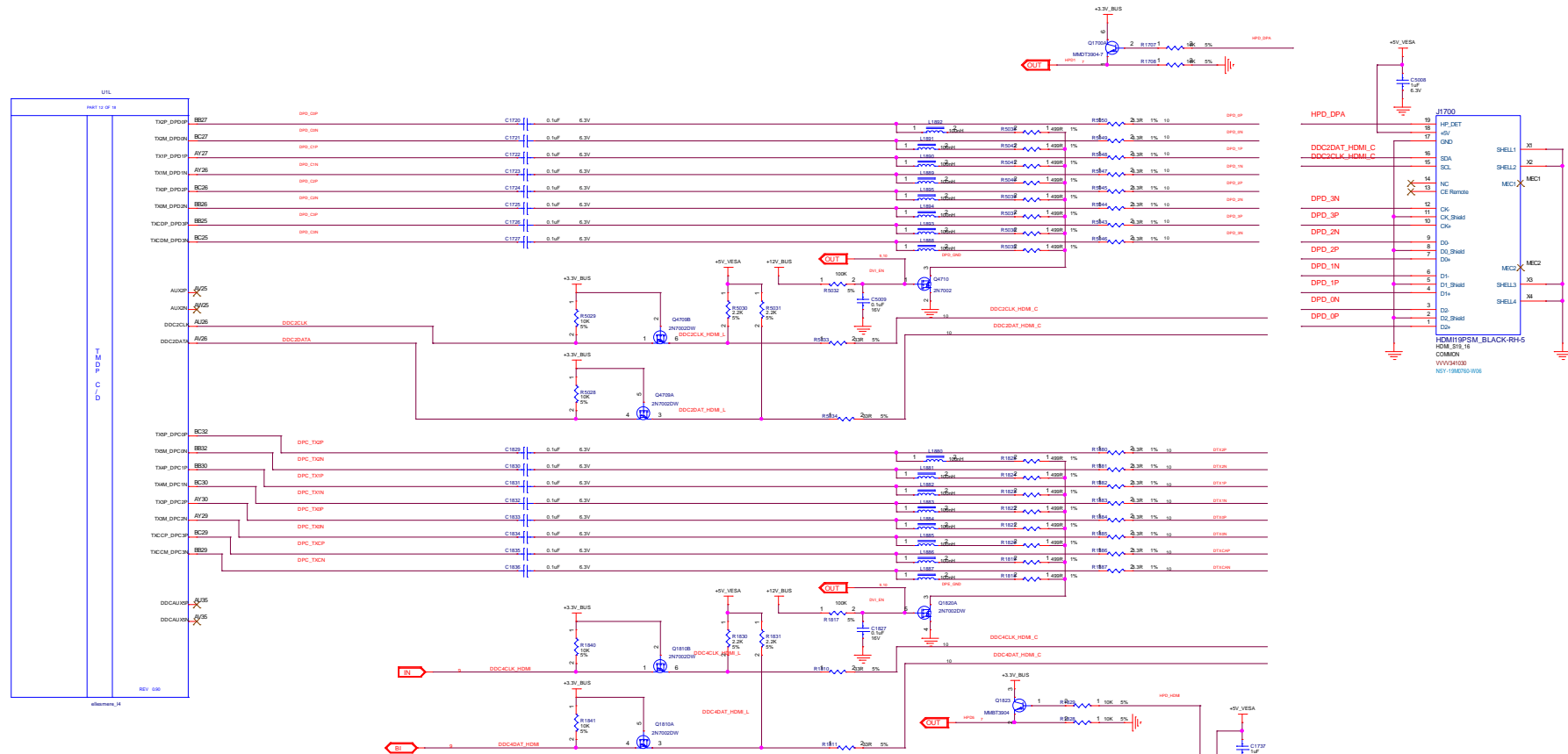


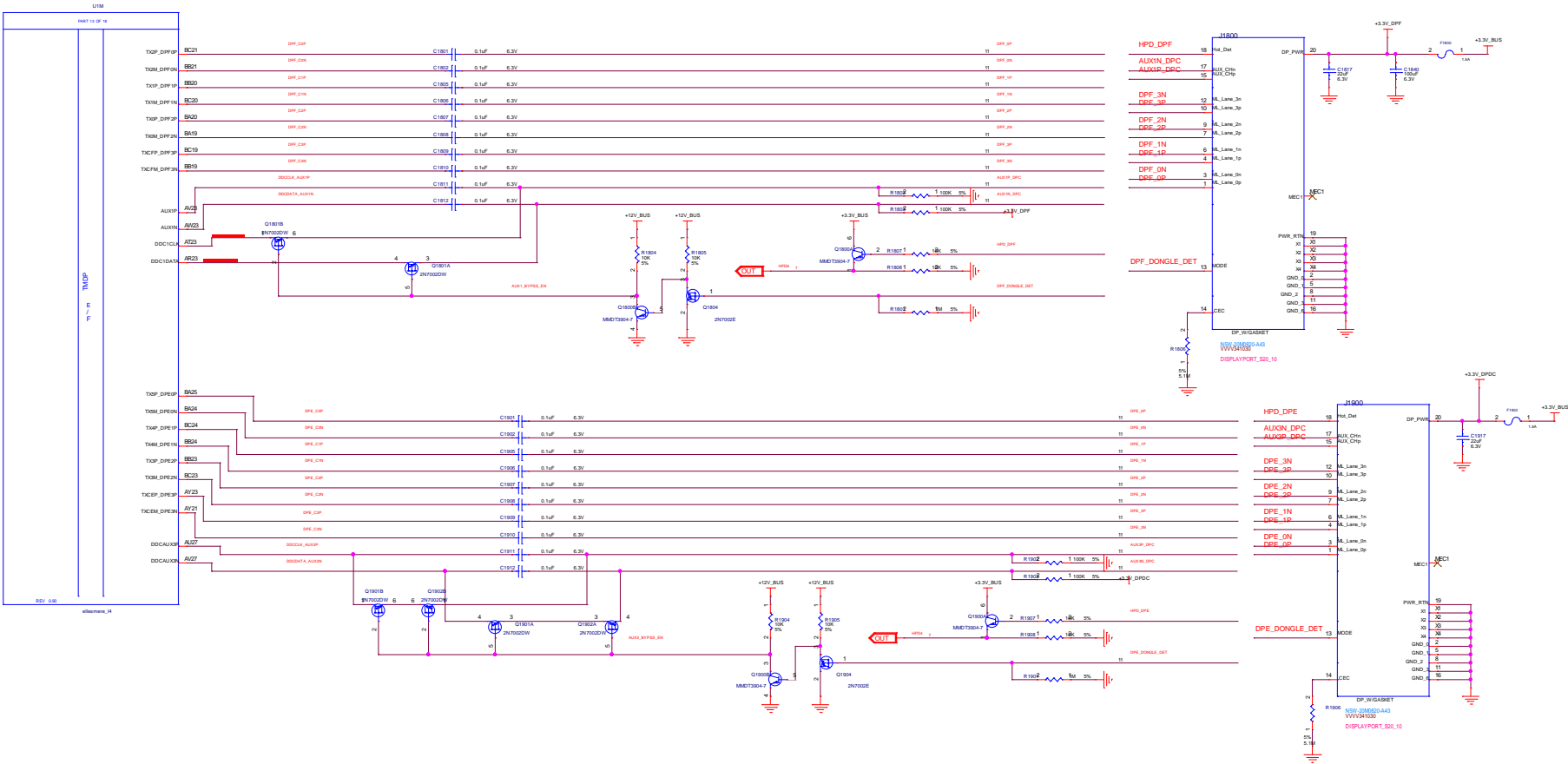
(9) ELLESMERE TMDP A/B



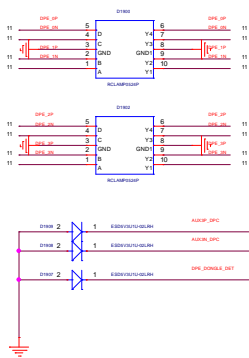
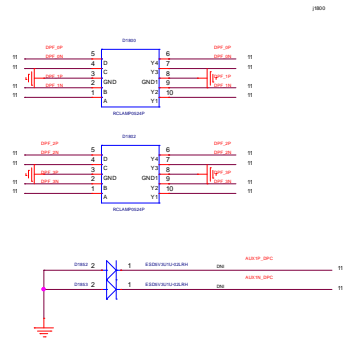
OPTIONAL ESD PROTECTION DIODES



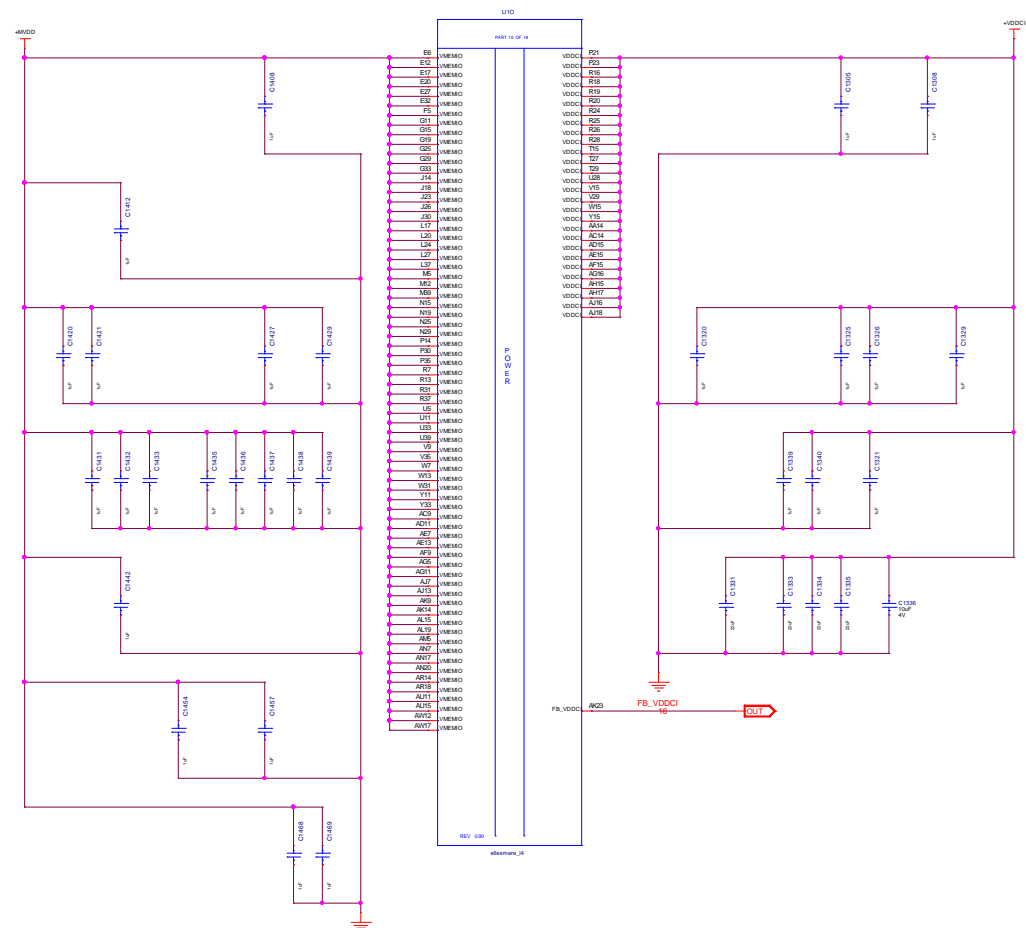
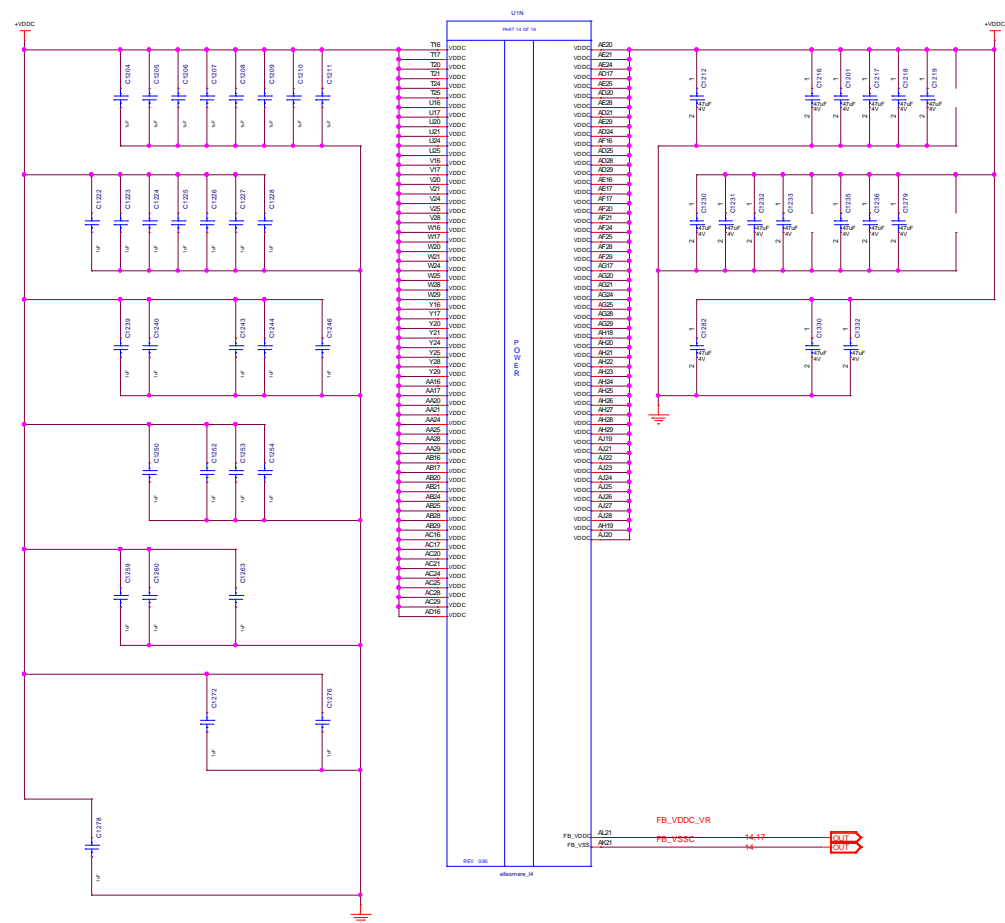




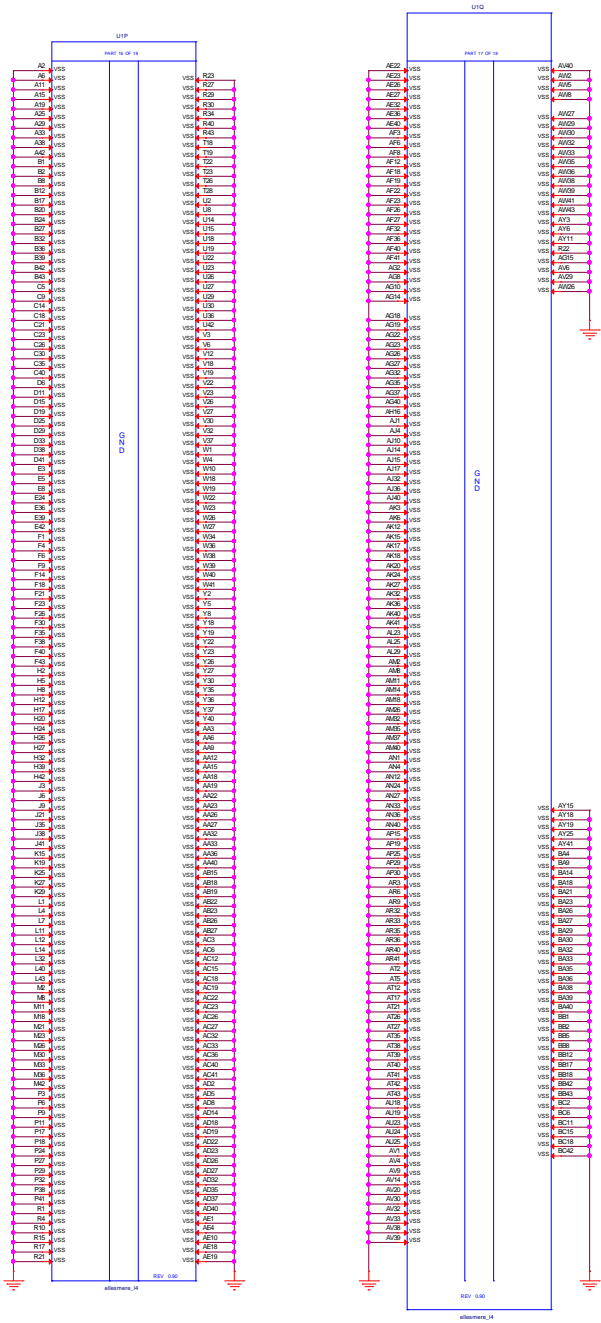
OPTIONAL ESD PROTECTION DIODES



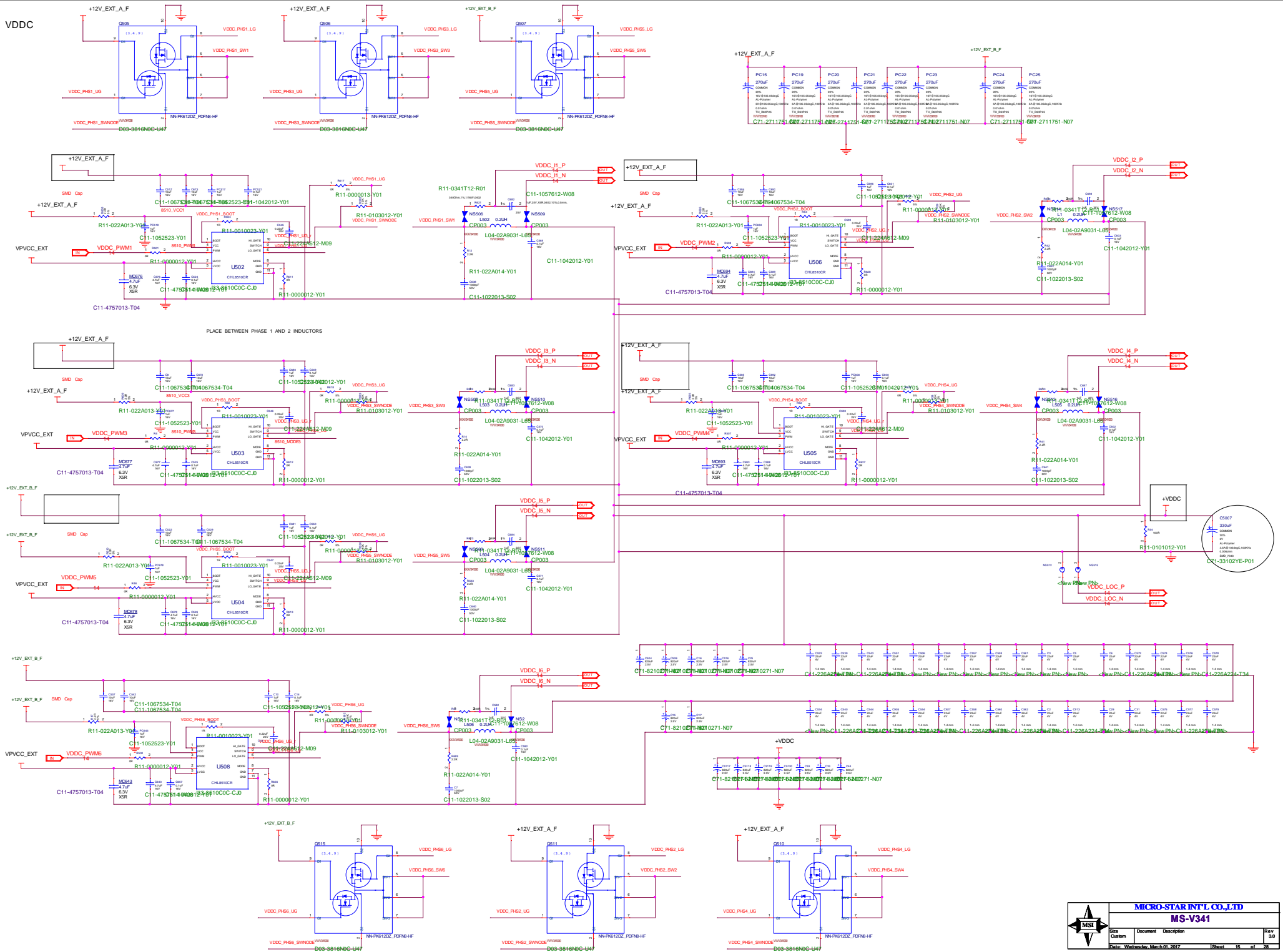
(12) ELLESMERE POWER

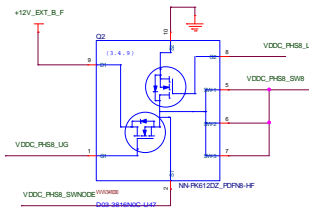
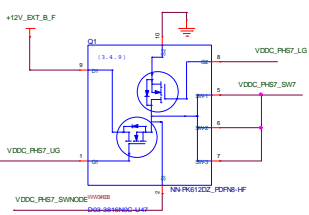
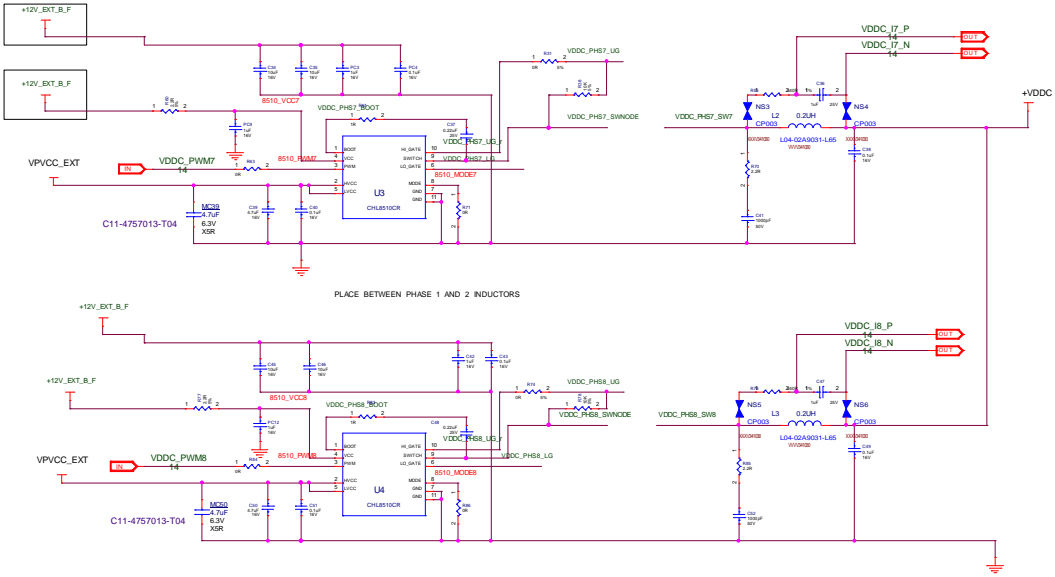


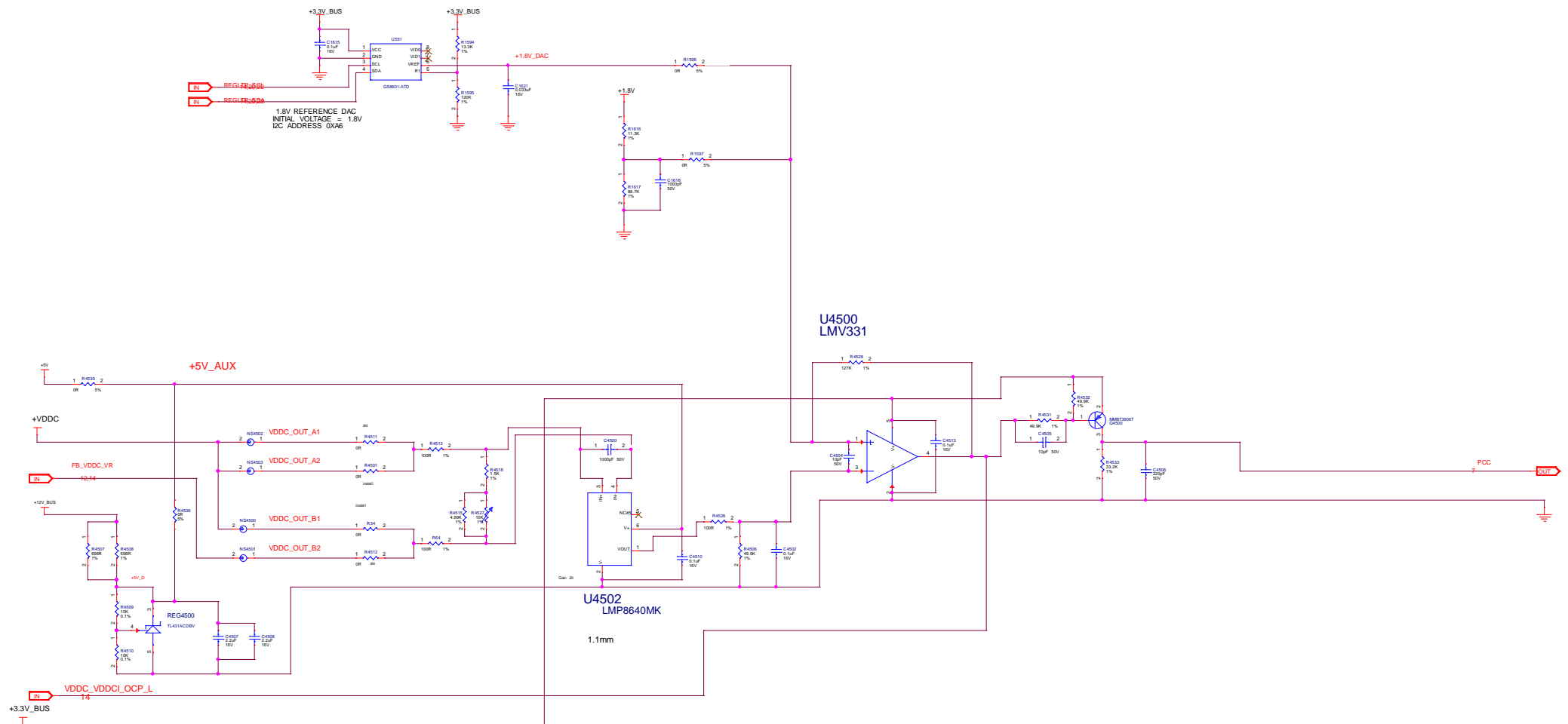
(13) ELLESMERE GROUND

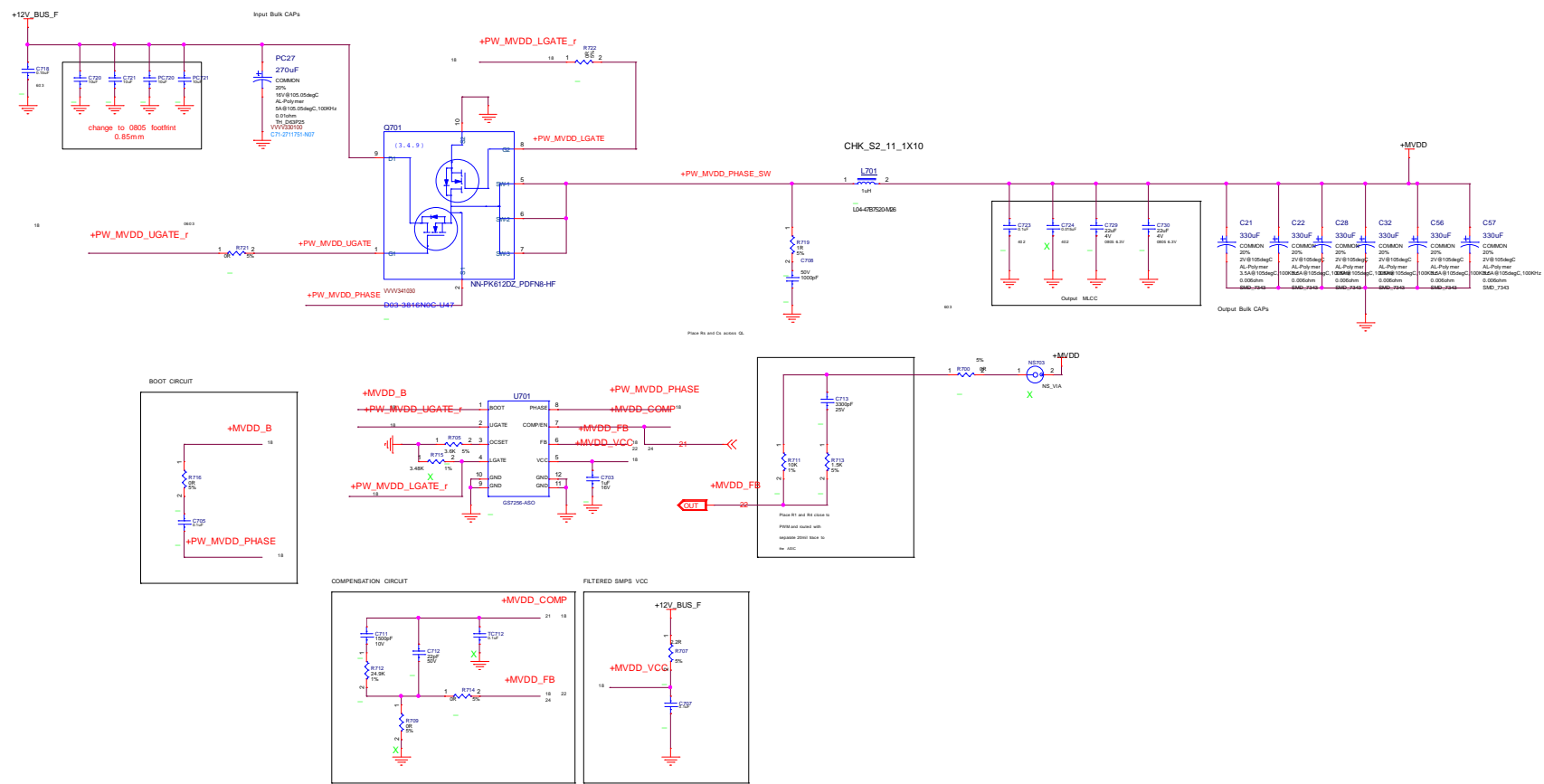


VDDC

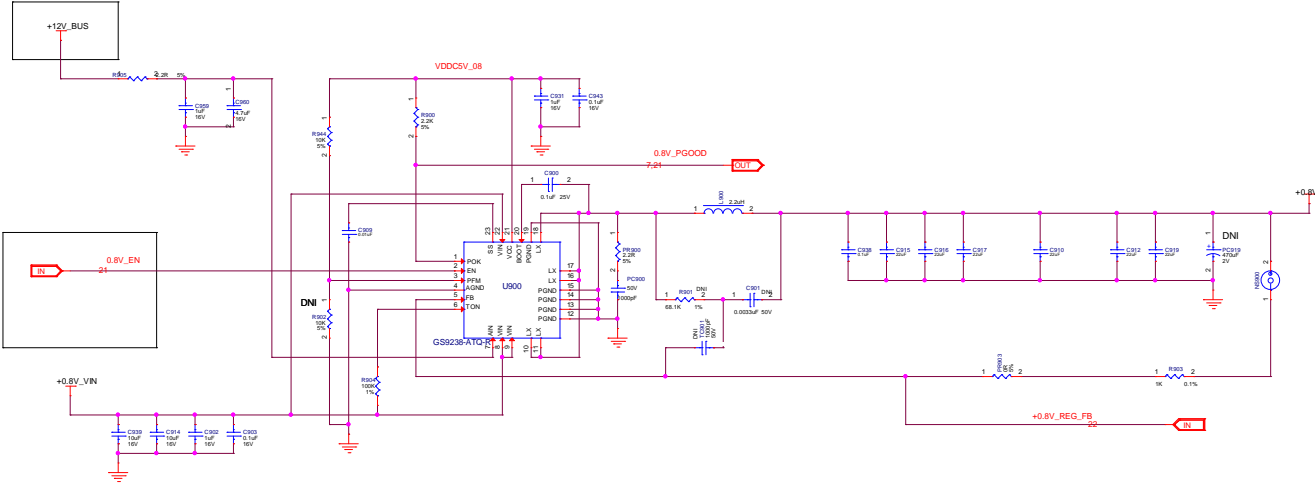








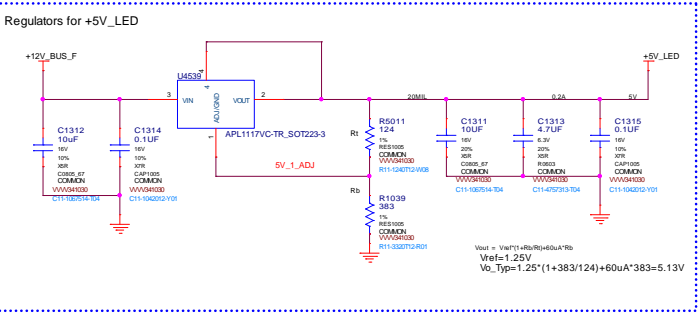
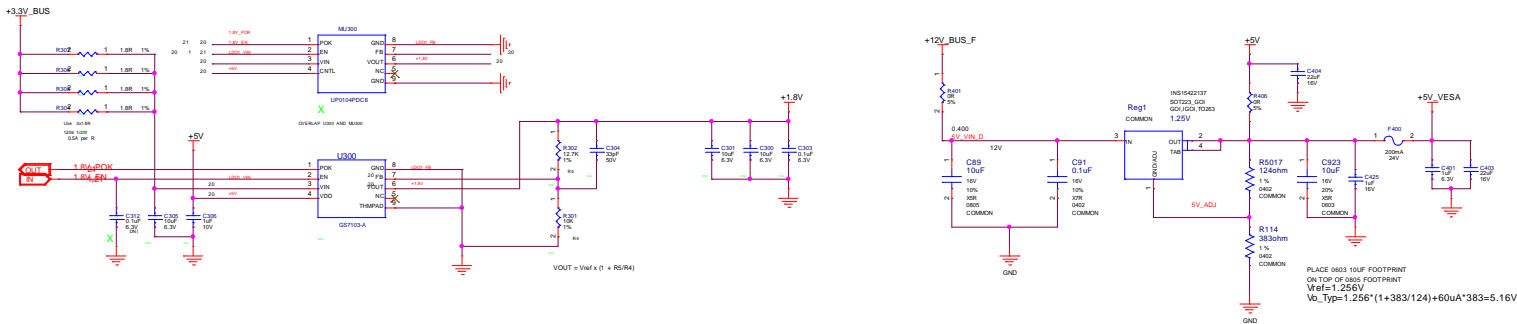
(17) 0.95V



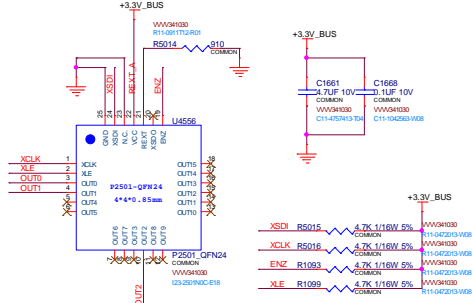
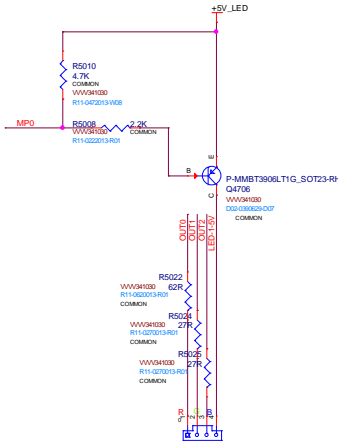
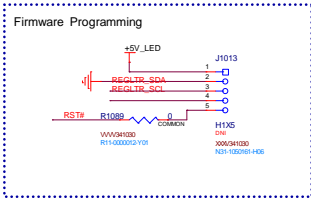
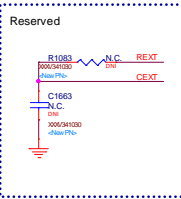
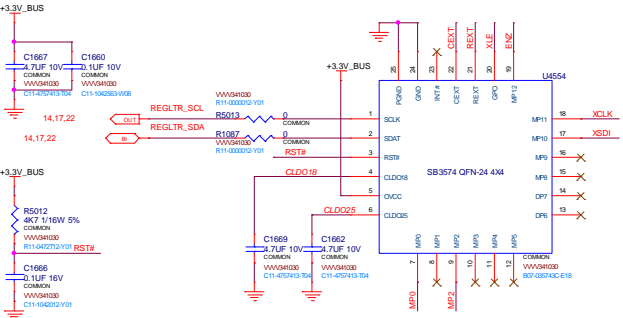
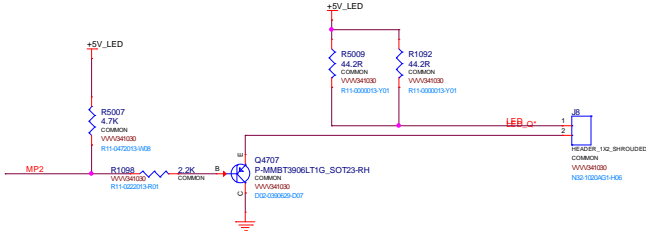
(18) SMALL RAIL REGULATORS , LED_CONTROLLER_ENE3574

LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

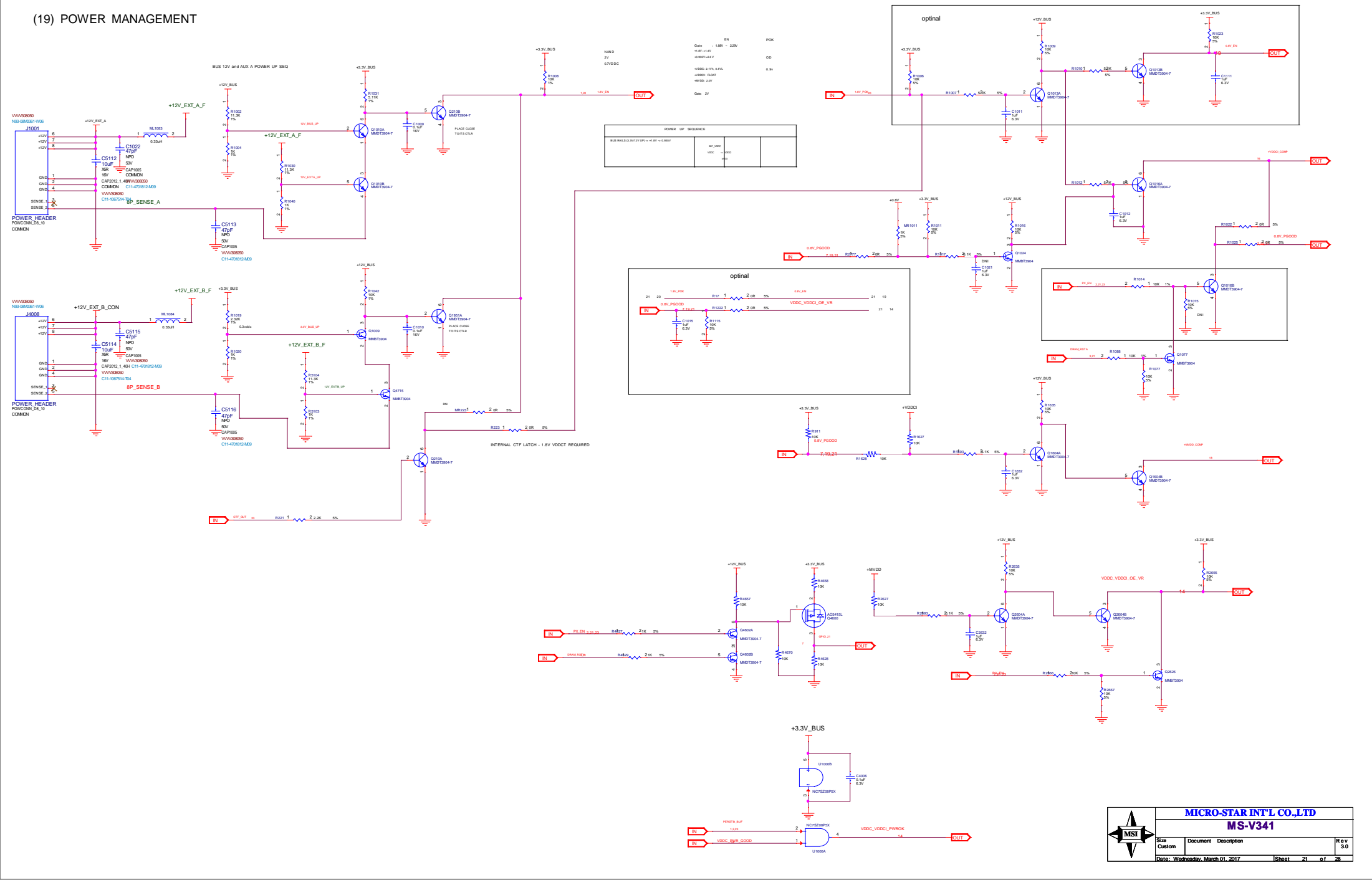
REGULATOR FOR +5V Rails
IOUT MAX = 150mA

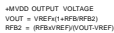
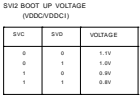


LED Connector

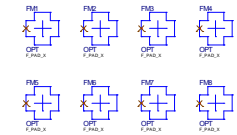
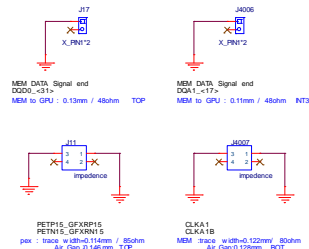
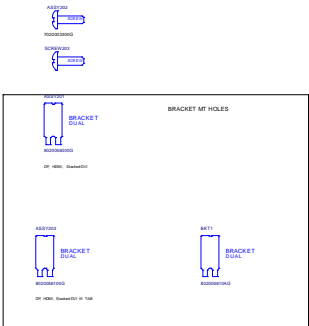
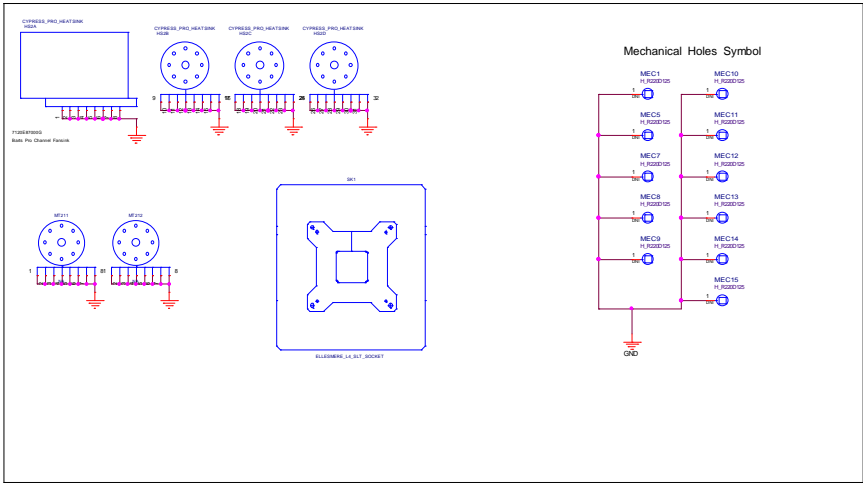
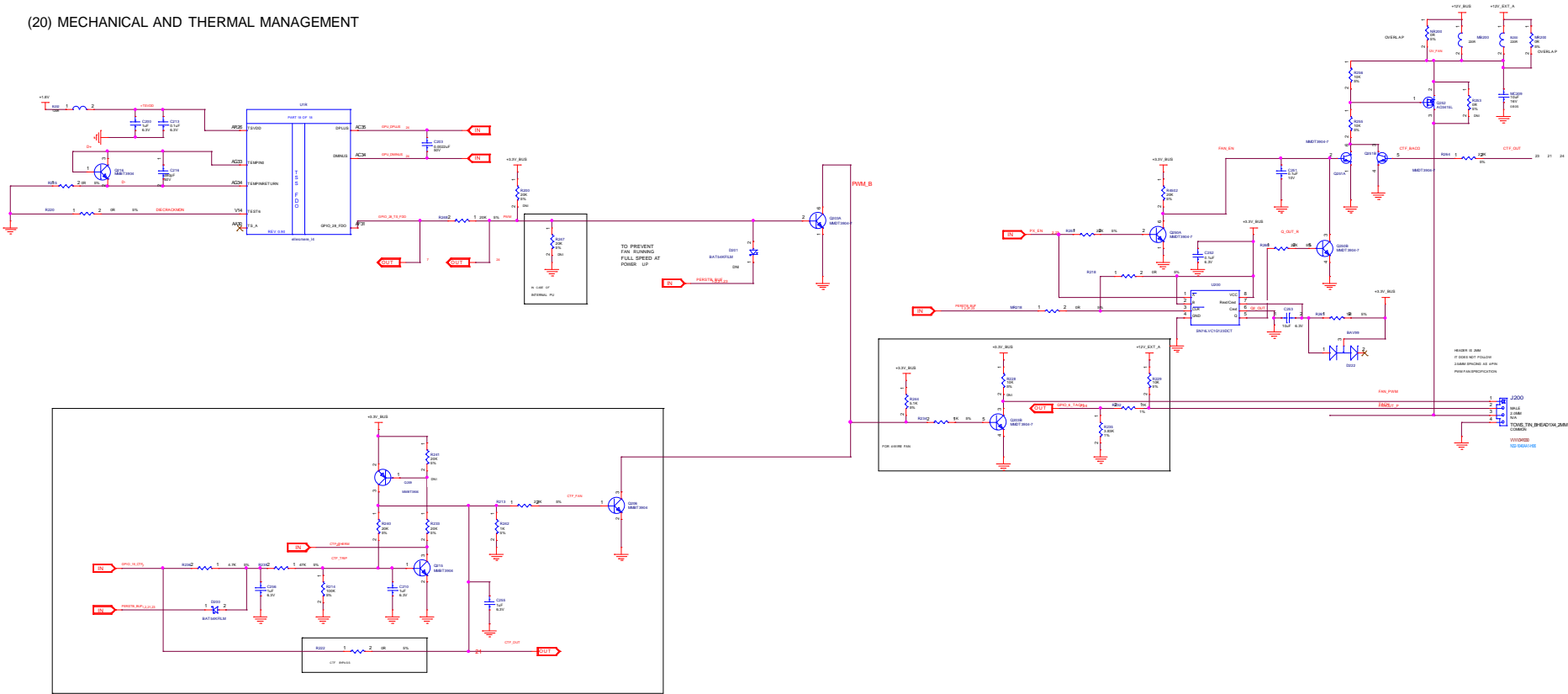


(19) POWER MANAGEMENT

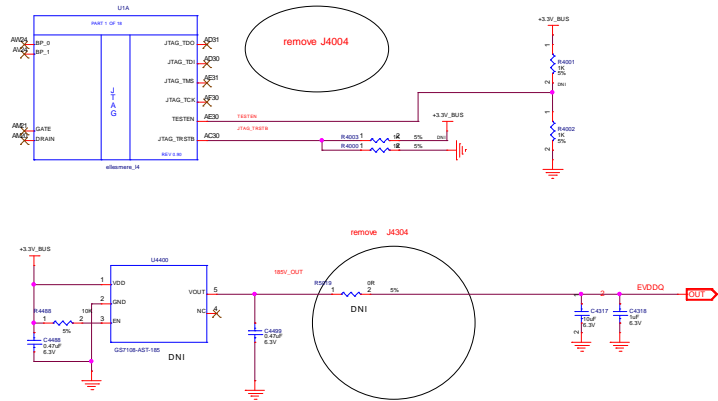




(20) MECHANICAL AND THERMAL MANAGEMENT



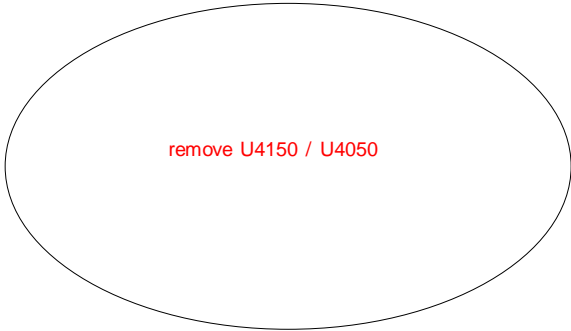
JTAG



E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)

DIGITAL POTS

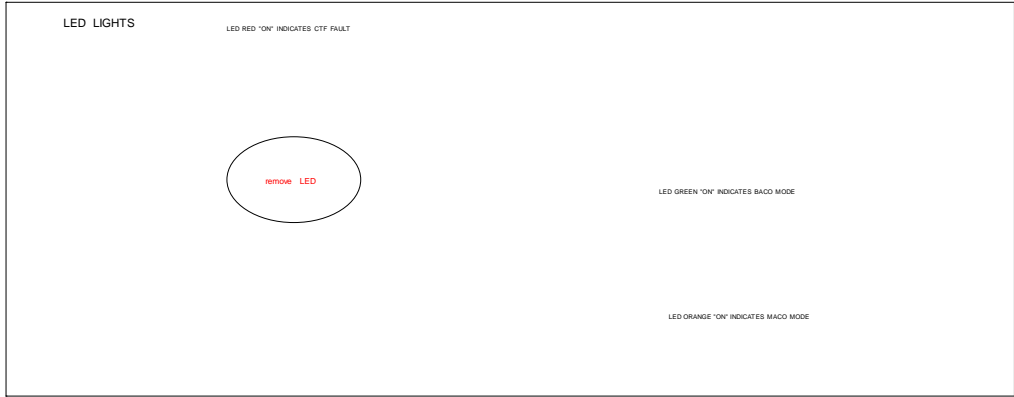
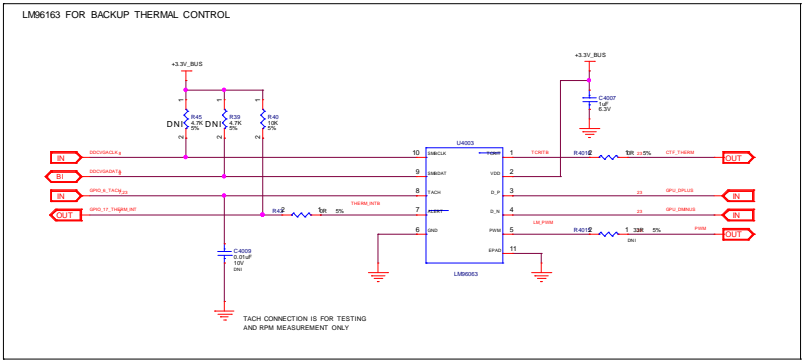
DIGITAL POTS



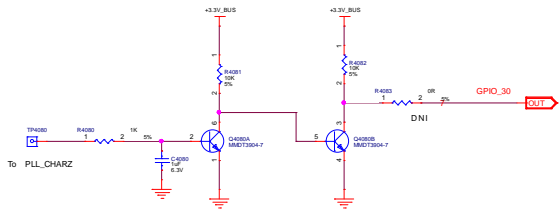
SWITCHES



BYPASS/ENABLE CTF
MAXIMIZE FAN



VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS



0	00A	0000001	
1	00B	0000002	1. Add EPICOT for G4000, VDDC2 2. Update VDDC clock frequency
2	00C	0000003	HD001: - add screen indicator R18B3 -> R18B7 - add pad screen indicator L18B3 -> L18B7 - remove C4B5, C4B3, VR4B5C4B3C4B4B5

- Page7: remove J2,J3
- Page9: enable DVI
- Page10: DP change to HDMI
- Page15: change dual-N MOS
- Page16: change dual-N MOS
- Page18: change dual-N MOS
- Page20: Add LED circuit
- Page21: colay 8pin power connecter
- Page24: remove debug circuit
- V341-3.0 I an
14. VDDC change IR35201
- 15.更改SMD電容改DIP電容
- 16.配合高度更換output SMD電感及電容
- 18.配合高度更換output SMD電感及電容
- 20.背板LED移除
- 27.增加7 / 8相

